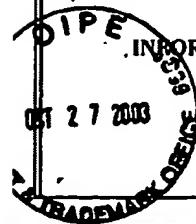


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		APPLICANT Oğuz Tanrikulu					
		FILING DATE March 19, 2001	GROUP 2631				
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EXAM- INER INI- TIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB- CLASS	FILING DATE IF APPROPRIATE
	AA				RECEIVED		
	AB				JUL 31 2001		
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	AD						
	AE						
	AF						
FOREIGN PATENT DOCUMENTS							
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	AL						
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	AN						
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<i>TJH</i>	AR	Mitra, S.K. and Hirano, K., "Digital All-Pass Networks," IEEE Trans. Circuits Systs., CAS-21(5): 688-700 (1974) 12/13/74					
<i>TJH</i>	AS	Tanrikulu, O. and Kalkan, M., "Design and Discrete Re-optimization of All-pass Based Power Symmetric IIR Filters," Electronics Letters, 32(16): 1458-1460 (1996) 8/96 NUDAY					
<i>TJH</i>	AT	Tanrikulu, O., "Adaptive Signal Processing Algorithms with Accelerated Convergence and Noise Immunity," Ph.D. Thesis, Imperial College of Science, Technology and Medicine, University of London (1995) 6/95 NUDAY					
<i>TJH</i>	AU	Valenzuela, R.A and Constantinides, A.G., "Digital Signal Processing Schemes for Efficient Interpolation and Decimation," IEE Proceedings-G, Circuits Devices and Systems, 130(6): 225-235 (1983) 12/83 NUDAY					
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	AH2			
	AI2			
	AJ2			
	AK2			
	AA3			
	AB3			
	AC3			

EXAMINER <i>Tiffany Ward</i>	DATE CONSIDERED <i>11/28/03</i>
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Oğuz Tanrikulu

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	AE			
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INFORMATION DISCLOSURE CITATION IN AN APPLICATION	APPLICANT Oğuz Tanrikulu	
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		GROUP 2644

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EXAMINER <i>Tiffey Harstel</i>	DATE CONSIDERED <i>11/28/03</i>
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